

## Jeu d'instructions du Z80A

Opcode	Instruction	Cycles nop	Préfixe #CB	Cycles nop	Préfixe #DD	Cycles nop	Préfixe #ED	Cycles nop	Préfixe #FD	Cycles nop	Préfixe #DDCB	Cycles nop	Préfixe #FDCB	Cycles nop
#00	NOP	1	RLC B	2							* LD B,RLC (IX+d)	7	* LD B,RLC (IY+d)	7
#01	LD BC,nn	3	RLC C	2							* LD C,RLC (IX+d)	7	* LD C,RLC (IY+d)	7
#02	LD (BC),A	2	RLC D	2							* LD D,RLC (IX+d)	7	* LD D,RLC (IY+d)	7
#03	INC BC	2	RLC E	2							* LD E,RLC (IX+d)	7	* LD E,RLC (IY+d)	7
#04	INC B	1	RLC H	2							* LD H,RLC (IX+d)	7	* LD H,RLC (IY+d)	7
#05	DEC B	1	RLC L	2							* LD L,RLC (IX+d)	7	* LD L,RLC (IY+d)	7
#06	LD B,n	2	RLC (HL)	3							RLC (IX+d)	7	RLC (IY+d)	7
#07	RLCA	1	RLC A	2							* LD A,RLC (IX+d)	7	* LD A,RLC (IY+d)	7
#08	EX AF,AF	1	RRC B	2							* LD B,RRC (IX+d)	7	* LD B,RRC (IY+d)	7
#09	ADD HL,BC	3	RRC C	2	ADD IX,BC	4			ADD IY,BC	4	* LD C,RRC (IX+d)	7	* LD C,RRC (IY+d)	7
#0A	LD A,(BC)	2	RRC D	2							* LD D,RRC (IX+d)	7	* LD D,RRC (IY+d)	7
#0B	DEC BC	2	RRC E	2							* LD E,RRC (IX+d)	7	* LD E,RRC (IY+d)	7
#0C	INC C	1	RRC H	2							* LD H,RRC (IX+d)	7	* LD H,RRC (IY+d)	7
#0D	DEC C	1	RRC L	2							* LD L,RRC (IX+d)	7	* LD L,RRC (IY+d)	7
#0E	LD C,n	2	RRC (HL)	3							RRC (IX+d)	7	RRC (IY+d)	7
#0F	RRC A	1	RRC A	2							* LD A,RRC (IX+d)	7	* LD A,RRC (IY+d)	7
#10	DJNZ d	3   4	RL B	2							* LD B,RL (IX+d)	7	* LD B,RL (IY+d)	7
#11	LD DE,nn	3	RL C	2							* LD C,RL (IX+d)	7	* LD C,RL (IY+d)	7
#12	LD (DE),A	2	RL D	2							* LD D,RL (IX+d)	7	* LD D,RL (IY+d)	7
#13	INC DE	2	RL E	2							* LD E,RL (IX+d)	7	* LD E,RL (IY+d)	7
#14	INC D	1	RL H	2							* LD H,RL (IX+d)	7	* LD H,RL (IY+d)	7
#15	DEC D	1	RL L	2							* LD L,RL (IX+d)	7	* LD L,RL (IY+d)	7
#16	LD D,n	2	RL (HL)	3							RL (IX+d)	7	RL (IY+d)	7
#17	RLA	1	RL A	2							* LD A,RL (IX+d)	7	* LD A,RL (IY+d)	7
#18	JR d	3	RR B	2							* LD B,RR (IX+d)	7	* LD B,RR (IY+d)	7
#19	ADD HL,DE	3	RR C	2	ADD IX,DE	4			ADD IY,DE	4	* LD C,RR (IX+d)	7	* LD C,RR (IY+d)	7
#1A	LD A,(DE)	2	RR D	2							* LD D,RR (IX+d)	7	* LD D,RR (IY+d)	7
#1B	DEC DE	2	RR E	2							* LD E,RR (IX+d)	7	* LD E,RR (IY+d)	7
#1C	INC E	1	RR H	2							* LD H,RR (IX+d)	7	* LD H,RR (IY+d)	7
#1D	DEC E	1	RR L	2							* LD L,RR (IX+d)	7	* LD L,RR (IY+d)	7
#1E	LD E,n	2	RR (HL)	3							RR (IX+d)	7	RR (IY+d)	7
#1F	RRA	1	RR A	2							* LD A,RR (IX+d)	7	* LD A,RR (IY+d)	7
#20	JR NZ,d	3   2	SRA B	2							* LD B,SRA (IX+d)	7	* LD B,SRA (IY+d)	7
#21	LD HL,nn	3	SRA C	2	LD IX,nn	4			LD IY,nn	4	* LD C,SRA (IX+d)	7	* LD C,SRA (IY+d)	7
#22	LD (nn),HL	5	SRA D	2	LD (nn),IX	6			LD (nn),IY	6	* LD D,SRA (IX+d)	7	* LD D,SRA (IY+d)	7
#23	INC HL	2	SRA E	2	INC IX	3			INC IY	3	* LD E,SRA (IX+d)	7	* LD E,SRA (IY+d)	7
#24	INC H	1	SRA H	2	* INC IXH	2			* INC IYH	2	* LD H,SRA (IX+d)	7	* LD H,SRA (IY+d)	7
#25	DEC H	1	SRA L	2	* DEC IXH	2			* DEC IYH	2	* LD L,SRA (IX+d)	7	* LD L,SRA (IY+d)	7
#26	LD H,n	2	SRA (HL)	3	* LD IXH,n	3			* LD IYH,n	3	SRA (IX+d)	7	SRA (IY+d)	7
#27	DAA	1	SRA A	2							* LD A,SRA (IX+d)	7	* LD A,SRA (IY+d)	7
#28	JR Z,d	3   2	SRA B	2							* LD B,SRA (IX+d)	7	* LD B,SRA (IY+d)	7
#29	ADD HL,HL	3	SRA C	2	ADD IX,IX	4			ADD IY,IY	4	* LD C,SRA (IX+d)	7	* LD C,SRA (IY+d)	7
#2A	LD HL,(nn)	5	SRA D	2	LD IX,(nn)	6			LD IY,(nn)	6	* LD D,SRA (IX+d)	7	* LD D,SRA (IY+d)	7
#2B	DEC HL	2	SRA E	2	DEC IX	3			DEC IY	3	* LD E,SRA (IX+d)	7	* LD E,SRA (IY+d)	7
#2C	INC L	1	SRA H	2	* INC IXL	2			* INC IYL	2	* LD H,SRA (IX+d)	7	* LD H,SRA (IY+d)	7
#2D	DEC L	1	SRA L	2	* DEC IXL	2			* DEC IYL	2	* LD L,SRA (IX+d)	7	* LD L,SRA (IY+d)	7
#2E	LD L,n	2	SRA (HL)	3	* LD IXL,n	3			* LD IYL,n	3	SRA (IX+d)	7	SRA (IY+d)	7
#2F	CPL	1	SRA A	2							* LD A,SRA (IX+d)	7	* LD A,SRA (IY+d)	7
#30	JR NC,d	3   2	* SLL B	2							* LD B,SLL (IX+d)	7	* LD B,SLL (IY+d)	7
#31	LD SP,nn	3	* SLL C	2							* LD C,SLL (IX+d)	7	* LD C,SLL (IY+d)	7
#32	LD (nn),A	4	* SLL D	2							* LD D,SLL (IX+d)	7	* LD D,SLL (IY+d)	7
#33	INC SP	2	* SLL E	2							* LD E,SLL (IX+d)	7	* LD E,SLL (IY+d)	7
#34	INC (HL)	3	* SLL H	2	INC (IX+d)	6			INC (IY+d)	4	* LD H,SLL (IX+d)	7	* LD H,SLL (IY+d)	7
#35	DEC (HL)	3	* SLL L	2	DEC (IX+d)	6			DEC (IY+d)	4	* LD L,SLL (IX+d)	7	* LD L,SLL (IY+d)	7
#36	LD (HL),n	3	* SLL (HL)	3	LD (IX+d),n	6			LD (IY+d),n	4	* SLL (IX+d)	7	* SLL (IY+d)	7
#37	SCF	1	* SLL A	2							* LD A,SLL (IX+d)	7	* LD A,SLL (IY+d)	7
#38	JR C,d	3   2	SRL B	2							* LD B,SRL (IX+d)	7	* LD B,SRL (IY+d)	7
#39	ADD HL,SP	3	SRL C	2	ADD IX,SP	4			ADD IY,SP	4	* LD C,SRL (IX+d)	7	* LD C,SRL (IY+d)	7
#3A	LD A,(nn)	4	SRL D	2							* LD D,SRL (IX+d)	7	* LD D,SRL (IY+d)	7
#3B	DEC SP	2	SRL E	2							* LD E,SRL (IX+d)	7	* LD E,SRL (IY+d)	7
#3C	INC A	1	SRL H	2							* LD H,SRL (IX+d)	7	* LD H,SRL (IY+d)	7
#3D	DEC A	1	SRL L	2							* LD L,SRL (IX+d)	7	* LD L,SRL (IY+d)	7
#3E	LD A,n	2	SRL (HL)	3							SRL (IX+d)	7	SRL (IY+d)	7
#3F	CCF	1	SRL A	2							* LD A,SRL (IX+d)	7	* LD A,SRL (IY+d)	7
#40	LD B,B	1	BIT 0,B	2			IN B,(C)	4						
#41	LD B,C	1	BIT 0,C	2			OUT (C),B	4						
#42	LD B,D	1	BIT 0,D	2			SBC HL,BC	4						
#43	LD B,E	1	BIT 0,E	2			LD (nn),BC	6						
#44	LD B,H	1	BIT 0,H	2	* LD B,IXH	2	NEG	2	* LD B,IYH	2				
#45	LD B,L	1	BIT 0,L	2	* LD B,IXL	2	RETN	4	* LD B,IYL	2				
#46	LD B,(HL)	2	BIT 0,(HL)	3	LD B,(IX+d)	5	IM 0	2	LD B,(IY+d)	3	BIT 0,(IX+d)	6	BIT 0,(IY+d)	6
#47	LD B,A	1	BIT 0,A	2			LD I,A	3						
#48	LD C,B	1	BIT 1,B	2			IN C,(C)	4						
#49	LD C,C	1	BIT 1,C	2			OUT (C),C	4						
#4A	LD C,D	1	BIT 1,D	2			ADC HL,BC	4						
#4B	LD C,E	1	BIT 1,E	2			LD BC,(nn)	6						
#4C	LD C,H	1	BIT 1,H	2	* LD C,IXH	2			* LD C,IYH	2				
#4D	LD C,L	1	BIT 1,L	2	* LD C,IXL	2	RETI	4	* LD C,IYL	2				
#4E	LD C,(HL)	2	BIT 1,(HL)	3	LD C,(IX+d)	5			LD C,(IY+d)	3	BIT 1,(IX+d)	6	BIT 1,(IY+d)	6
#4F	LD C,A	1	BIT 1,A	2			LD R,A	3						
#50	LD D,B	1	BIT 2,B	2			IN D,(C)	4						
#51	LD D,C	1	BIT 2,C	2			OUT (C),D	4						
#52	LD D,D	1	BIT 2,D	2			SBC HL,DE	4						
#53	LD D,E	1	BIT 2,E	2			LD (nn),DE	6						
#54	LD D,H	1	BIT 2,H	2	* LD D,IXH	2			* LD D,IYH	2				
#55	LD D,L	1	BIT 2,L	2	* LD D,IXL	2			* LD D,IYL	2				
#56	LD D,(HL)	2	BIT 2,(HL)	3	LD D,(IX+d)	5	IM 1	2	LD D,(IY+d)	3	BIT 2,(IX+d)	6	BIT 2,(IY+d)	6
#57	LD D,A	1	BIT 2,A	2			LD A,I	3						
#58	LD E,B	1	BIT 3,B	2			IN E,(C)	4						
#59	LD E,C	1	BIT 3,C	2			OUT (C),E	4						
#5A	LD E,D	1	BIT 3,D	2			ADC HL,DE	4						
#5B	LD E,E	1	BIT 3,E	2			LD DE,(nn)	6						

Les instructions précédées du caractère \* ne sont pas documentées.

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Opcode	Instruction	Cycles nop	Préfixe #CB	Cycles nop	Préfixe #DD	Cycles nop	Préfixe #ED	Cycles nop	Préfixe #FD	Cycles nop	Préfixe #DDCB	Cycles nop	Préfixe #FDCB	Cycles nop
#5C	LD E,H	1	BIT 3,H	2	* LD E,IXH	2			* LD E,IYH	2				
#5D	LD E,L	1	BIT 3,L	2	* LD E,IXL	2			* LD E,IYL	2				
#5E	LD E,(HL)	2	BIT 3,(HL)	3	LD E,(IX+d)	5	IM 2	2	LD E,(IY+d)	3	BIT 3,(IX+d)	6	BIT 3,(IY+d)	6
#5F	LD E,A	1	BIT 3,A	2			LD A,R	3						
#60	LD H,B	1	BIT 4,B	2	* LD IXH,B	2	IN H,(C)	4	* LD IYH,B	2				
#61	LD H,C	1	BIT 4,C	2	* LD IXH,C	2	OUT (C),H	4	* LD IYH,C	2				
#62	LD H,D	1	BIT 4,D	2	* LD IXH,D	2	SBC HL,HL	4	* LD IYH,D	2				
#63	LD H,E	1	BIT 4,E	2	* LD IXH,E	2			* LD IYH,E	2				
#64	LD H,H	1	BIT 4,H	2	* LD IXH,IXH	2			* LD IYH,IYH	2				
#65	LD H,L	1	BIT 4,L	2	* LD IXH,IXL	2			* LD IYH,IYL	2				
#66	LD H,(HL)	2	BIT 4,(HL)	3	LD H,(IX+d)	5			LD H,(IY+d)	3	BIT 4,(IX+d)	6	BIT 4,(IY+d)	6
#67	LD H,A	1	BIT 4,A	2	* LD IXH,A	2	RDD	5	* LD IYH,A	2				
#68	LD L,B	1	BIT 5,B	2	* LD IXL,B	2	IN L,(C)	4	* LD IYL,B	2				
#69	LD L,C	1	BIT 5,C	2	* LD IXL,C	2	OUT (C),L	4	* LD IYL,C	2				
#6A	LD L,D	1	BIT 5,D	2	* LD IXL,D	2	ADC HL,HL	4	* LD IYL,D	2				
#6B	LD L,E	1	BIT 5,E	2	* LD IXL,E	2			* LD IYL,E	2				
#6C	LD L,H	1	BIT 5,H	2	* LD IXL,IXH	2			* LD IYL,IYH	2				
#6D	LD L,L	1	BIT 5,L	2	* LD IXL,IXL	2			* LD IYL,IYL	2				
#6E	LD L,(HL)	2	BIT 5,(HL)	3	LD L,(IX+d)	5			LD L,(IY+d)	3	BIT 5,(IX+d)	6	BIT 5,(IY+d)	6
#6F	LD L,A	1	BIT 5,A	2	* LD IXL,A	2	RLD	5	* LD IYL,A	2				
#70	LD (HL),B	2	BIT 6,B	2	LD (IX+d),B	5	* IN F,(C)	4	LD (IY+d),B	3				
#71	LD (HL),C	2	BIT 6,C	2	LD (IX+d),C	5	* OUT (C),0	4	LD (IY+d),C	3				
#72	LD (HL),D	2	BIT 6,D	2	LD (IX+d),D	5	SBC HL,SP	4	LD (IY+d),D	3				
#73	LD (HL),E	2	BIT 6,E	2	LD (IX+d),E	5	LD (nn),SP	6	LD (IY+d),E	3				
#74	LD (HL),H	2	BIT 6,H	2	LD (IX+d),H	5			LD (IY+d),H	3				
#75	LD (HL),L	2	BIT 6,L	2	LD (IX+d),L	5			LD (IY+d),L	3				
#76	HALT	1	BIT 6,(HL)	3							BIT 6,(IX+d)	6	BIT 6,(IY+d)	6
#77	LD (HL),A	2	BIT 6,A	2	LD (IX+d),A	5			LD (IY+d),A	3				
#78	LD A,B	1	BIT 7,B	2			IN A,(C)	4						
#79	LD A,C	1	BIT 7,C	2			OUT (C),A	4						
#7A	LD A,D	1	BIT 7,D	2			ADC HL,SP	4						
#7B	LD A,E	1	BIT 7,E	2			LD SP,(nn)	6						
#7C	LD A,H	1	BIT 7,H	2	* LD A,IXH	2			* LD A,IYH	2				
#7D	LD A,L	1	BIT 7,L	2	* LD A,IXL	2			* LD A,IYL	2				
#7E	LD A,(HL)	2	BIT 7,(HL)	3	LD A,(IX+d)	5			LD A,(IY+d)	3	BIT 7,(IX+d)	6	BIT 7,(IY+d)	6
#7F	LD A,A	1	BIT 7,A	2										
#80	ADD A,B	1	RES 0,B	2							* LD B,RES 0,(IX+d)	7	* LD B,RES 0,(IY+d)	7
#81	ADD A,C	1	RES 0,C	2							* LD C,RES 0,(IX+d)	7	* LD C,RES 0,(IY+d)	7
#82	ADD A,D	1	RES 0,D	2							* LD D,RES 0,(IX+d)	7	* LD D,RES 0,(IY+d)	7
#83	ADD A,E	1	RES 0,E	2							* LD E,RES 0,(IX+d)	7	* LD E,RES 0,(IY+d)	7
#84	ADD A,H	1	RES 0,H	2	* ADD A,IXH	2			* ADD A,IYH	2	* LD H,RES 0,(IX+d)	7	* LD H,RES 0,(IY+d)	7
#85	ADD A,L	1	RES 0,L	2	* ADD A,IXL	2			* ADD A,IYL	2	* LD L,RES 0,(IX+d)	7	* LD L,RES 0,(IY+d)	7
#86	ADD A,(HL)	2	RES 0,(HL)	3	ADD A,(IX+d)	5			ADD A,(IY+d)	3	RES 0,(IX+d)	7	RES 0,(IY+d)	7
#87	ADD A,A	1	RES 0,A	2							* LD A,RES 0,(IX+d)	7	* LD A,RES 0,(IY+d)	7
#88	ADC A,B	1	RES 1,B	2							* LD B,RES 1,(IX+d)	7	* LD B,RES 1,(IY+d)	7
#89	ADC A,C	1	RES 1,C	2							* LD C,RES 1,(IX+d)	7	* LD C,RES 1,(IY+d)	7
#8A	ADC A,D	1	RES 1,D	2							* LD D,RES 1,(IX+d)	7	* LD D,RES 1,(IY+d)	7
#8B	ADC A,E	1	RES 1,E	2							* LD E,RES 1,(IX+d)	7	* LD E,RES 1,(IY+d)	7
#8C	ADC A,H	1	RES 1,H	2	* ADC A,IXH	2			* ADC A,IYH	2	* LD H,RES 1,(IX+d)	7	* LD H,RES 1,(IY+d)	7
#8D	ADC A,L	1	RES 1,L	2	* ADC A,IXL	2			* ADC A,IYL	2	* LD L,RES 1,(IX+d)	7	* LD L,RES 1,(IY+d)	7
#8E	ADC A,(HL)	2	RES 1,(HL)	3	ADC A,(IX+d)	5			ADC A,(IY+d)	3	RES 1,(IX+d)	7	RES 1,(IY+d)	7
#8F	ADC A,A	1	RES 1,A	2							* LD A,RES 1,(IX+d)	7	* LD A,RES 1,(IY+d)	7
#90	SUB B	1	RES 2,B	2							* LD B,RES 2,(IX+d)	7	* LD B,RES 2,(IY+d)	7
#91	SUB C	1	RES 2,C	2							* LD C,RES 2,(IX+d)	7	* LD C,RES 2,(IY+d)	7
#92	SUB D	1	RES 2,D	2							* LD D,RES 2,(IX+d)	7	* LD D,RES 2,(IY+d)	7
#93	SUB E	1	RES 2,E	2							* LD E,RES 2,(IX+d)	7	* LD E,RES 2,(IY+d)	7
#94	SUB H	1	RES 2,H	2	* SUB IXH	2			* SUB IYH	2	* LD H,RES 2,(IX+d)	7	* LD H,RES 2,(IY+d)	7
#95	SUB L	1	RES 2,L	2	* SUB IXL	2			* SUB IYL	2	* LD L,RES 2,(IX+d)	7	* LD L,RES 2,(IY+d)	7
#96	SUB (HL)	2	RES 2,(HL)	3	SUB (IX+d)	5			SUB (IY+d)	3	RES 2,(IX+d)	7	RES 2,(IY+d)	7
#97	SUB A	1	RES 2,A	2							* LD A,RES 2,(IX+d)	7	* LD A,RES 2,(IY+d)	7
#98	SBC A,B	1	RES 3,B	2							* LD B,RES 3,(IX+d)	7	* LD B,RES 3,(IY+d)	7
#99	SBC A,C	1	RES 3,C	2							* LD C,RES 3,(IX+d)	7	* LD C,RES 3,(IY+d)	7
#9A	SBC A,D	1	RES 3,D	2							* LD D,RES 3,(IX+d)	7	* LD D,RES 3,(IY+d)	7
#9B	SBC A,E	1	RES 3,E	2							* LD E,RES 3,(IX+d)	7	* LD E,RES 3,(IY+d)	7
#9C	SBC A,H	1	RES 3,H	2	* SBC A,IXH	2			* SBC A,IYH	2	* LD H,RES 3,(IX+d)	7	* LD H,RES 3,(IY+d)	7
#9D	SBC A,L	1	RES 3,L	2	* SBC A,IXL	2			* SBC A,IYL	2	* LD L,RES 3,(IX+d)	7	* LD L,RES 3,(IY+d)	7
#9E	SBC A,(HL)	2	RES 3,(HL)	3	SBC A,(IX+d)	5			SBC A,(IY+d)	3	RES 3,(IX+d)	7	RES 3,(IY+d)	7
#9F	SBC A,A	1	RES 3,A	2							* LD A,RES 3,(IX+d)	7	* LD A,RES 3,(IY+d)	7
#A0	AND B	1	RES 4,B	2			LDI	5			* LD B,RES 4,(IX+d)	7	* LD B,RES 4,(IY+d)	7
#A1	AND C	1	RES 4,C	2			CPI	4			* LD C,RES 4,(IX+d)	7	* LD C,RES 4,(IY+d)	7
#A2	AND D	1	RES 4,D	2			INI	5			* LD D,RES 4,(IX+d)	7	* LD D,RES 4,(IY+d)	7
#A3	AND E	1	RES 4,E	2			OUTI	5			* LD E,RES 4,(IX+d)	7	* LD E,RES 4,(IY+d)	7
#A4	AND H	1	RES 4,H	2	* AND IXH	2			* AND IYH	2	* LD H,RES 4,(IX+d)	7	* LD H,RES 4,(IY+d)	7
#A5	AND L	1	RES 4,L	2	* AND IXL	2			* AND IYL	2	* LD L,RES 4,(IX+d)	7	* LD L,RES 4,(IY+d)	7
#A6	AND (HL)	2	RES 4,(HL)	3	AND (IX+d)	5			AND (IY+d)	3	RES 4,(IX+d)	7	RES 4,(IY+d)	7
#A7	AND A	1	RES 4,A	2							* LD A,RES 4,(IX+d)	7	* LD A,RES 4,(IY+d)	7
#A8	XOR B	1	RES 5,B	2			LDD	5			* LD B,RES 5,(IX+d)	7	* LD B,RES 5,(IY+d)	7
#A9	XOR C	1	RES 5,C	2			CPD	4			* LD C,RES 5,(IX+d)	7	* LD C,RES 5,(IY+d)	7
#AA	XOR D	1	RES 5,D	2			IND	5			* LD D,RES 5,(IX+d)	7	* LD D,RES 5,(IY+d)	7
#AB	XOR E	1	RES 5,E	2			OUTD	5			* LD E,RES 5,(IX+d)	7	* LD E,RES 5,(IY+d)	7
#AC	XOR H	1	RES 5,H	2	* XOR IXH	2			* XOR IYH	2	* LD H,RES 5,(IX+d)	7	* LD H,RES 5,(IY+d)	7
#AD	XOR L	1	RES 5,L	2	* XOR IXL	2			* XOR IYL	2	* LD L,RES 5,(IX+d)	7	* LD L,RES 5,(IY+d)	7
#AE	XOR (HL)	2	RES 5,(HL)	3	XOR (IX+d)	5			XOR (IY+d)	3	RES 5,(IX+d)	7	RES 5,(IY+d)	7
#AF	XOR A	1	RES 5,A	2							* LD A,RES 5,(IX+d)	7	* LD A,RES 5,(IY+d)	7
#B0	OR B	1	RES 6,B	2			LDIR	6	5		* LD B,RES 6,(IX+d)	7	* LD B,RES 6,(IY+d)	7
#B1	OR C	1	RES 6,C	2			CPIR	6	4		* LD C,RES 6,(IX+d)	7	* LD C,RES 6,(IY+d)	7
#B2	OR D	1	RES 6,D	2			INIR	6	5		* LD D,RES 6,(IX+d)	7	* LD D,RES 6,(IY+d)	7
#B3	OR E	1	RES 6,E	2			OTIR	6	5		* LD E,RES 6,(IX+d)	7	* LD E,RES 6,(IY+d)	7
#B4	OR H	1	RES 6,H	2	* OR IXH	2			* OR IYH	2	* LD H,RES 6,(IX+d)	7	* LD H,RES 6,(IY+d)	7
#B5	OR L	1	RES 6,L	2	* OR IXL	2			* OR IYL	2	* LD L,RES 6,(IX+d)	7	* LD L,RES 6,(IY+d)	7
#B6	OR (HL)	2	RES 6,(HL)	3	OR (IX+d)	5			OR (IY+d)	3	RES 6,(IX+d)	7	RES 6,(IY+d)	7
#B7	OR A	1	RES 6,A	2							* LD A,RES 6,(IX+d)	7	* LD A,RES 6,(IY+d)	7

Les instructions précédées du caractère \* ne sont pas documentées.

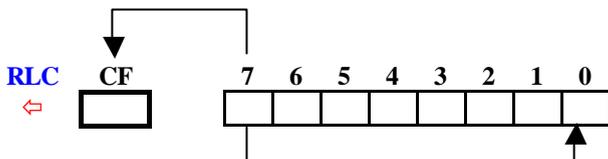
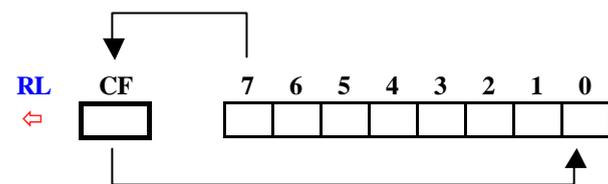
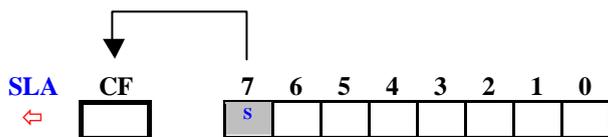
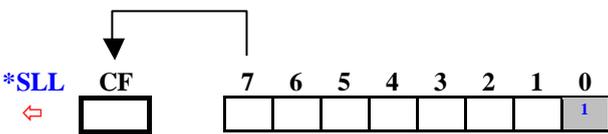
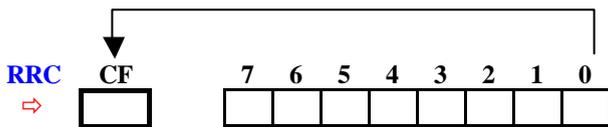
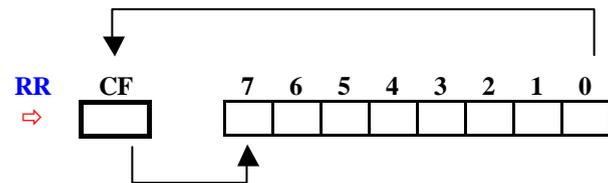
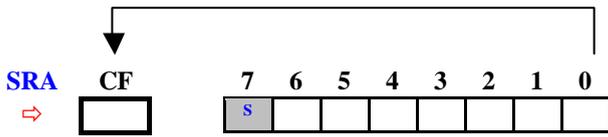
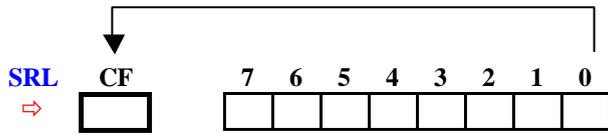
## Jeu d'instructions du Z80A

Opcode	Instruction	Cycles nop	Préfixe #CB	Cycles nop	Préfixe #DD	Cycles nop	Préfixe #ED	Cycles nop	Préfixe #FD	Cycles nop	Préfixe #DDCB	Cycles nop	Préfixe #FDCB	Cycles nop
#B8	CP B	1	RES 7,B	2			LDDR	6   5			* LD B,RES 7,(IX+d)	7	* LD B,RES 7,(IY+d)	7
#B9	CP C	1	RES 7,C	2			CPDR	6   4			* LD C,RES 7,(IX+d)	7	* LD C,RES 7,(IY+d)	7
#BA	CP D	1	RES 7,D	2			INDR	6   5			* LD D,RES 7,(IX+d)	7	* LD D,RES 7,(IY+d)	7
#BB	CP E	1	RES 7,E	2			OTDR	6   5			* LD E,RES 7,(IX+d)	7	* LD E,RES 7,(IY+d)	7
#BC	CP H	1	RES 7,H	2	* CP IXH	2			* CP IYH	2	* LD H,RES 7,(IX+d)	7	* LD H,RES 7,(IY+d)	7
#BD	CP L	1	RES 7,L	2	* CP IXL	2			* CP IYL	2	* LD L,RES 7,(IX+d)	7	* LD L,RES 7,(IY+d)	7
#BE	CP (HL)	2	RES 7,(HL)	3	CP (IX+d)	5			CP (IY+d)	3	RES 7,(IX+d)	7	RES 7,(IY+d)	7
#BF	CP A	1	RES 7,A	2							* LD A,RES 7,(IX+d)	7	* LD A,RES 7,(IY+d)	7
#C0	RET NZ	4   2	SET 0,B	2							* LD B,SET 0,(IX+d)	7	* LD B,SET 0,(IY+d)	7
#C1	POP BC	3	SET 0,C	2							* LD C,SET 0,(IX+d)	7	* LD C,SET 0,(IY+d)	7
#C2	JP NZ,nn	3	SET 0,D	2							* LD D,SET 0,(IX+d)	7	* LD D,SET 0,(IY+d)	7
#C3	JP nn	3	SET 0,E	2							* LD E,SET 0,(IX+d)	7	* LD E,SET 0,(IY+d)	7
#C4	CALL NZ,nn	5   3	SET 0,H	2							* LD H,SET 0,(IX+d)	7	* LD H,SET 0,(IY+d)	7
#C5	PUSH BC	4	SET 0,L	2							* LD L,SET 0,(IX+d)	7	* LD L,SET 0,(IY+d)	7
#C6	ADD A,n	2	SET 0,(HL)	3							SET 0,(IX+d)	7	SET 0,(IY+d)	7
#C7	RST #00	4	SET 0,A	2							* LD A,SET 0,(IX+d)	7	* LD A,SET 0,(IY+d)	7
#C8	RET Z	4   2	SET 1,B	2							* LD B,SET 1,(IX+d)	7	* LD B,SET 1,(IY+d)	7
#C9	RET	3	SET 1,C	2							* LD C,SET 1,(IX+d)	7	* LD C,SET 1,(IY+d)	7
#CA	JP Z,nn	3	SET 1,D	2							* LD D,SET 1,(IX+d)	7	* LD D,SET 1,(IY+d)	7
#CB			SET 1,E	2							* LD E,SET 1,(IX+d)	7	* LD E,SET 1,(IY+d)	7
#CC	CALL Z,nn	5   3	SET 1,H	2							* LD H,SET 1,(IX+d)	7	* LD H,SET 1,(IY+d)	7
#CD	CALL nn	5	SET 1,L	2							* LD L,SET 1,(IX+d)	7	* LD L,SET 1,(IY+d)	7
#CE	ADC A,n	2	SET 1,(HL)	3							SET 1,(IX+d)	7	SET 1,(IY+d)	7
#CF	RST #08	4	SET 1,A	2							* LD A,SET 1,(IX+d)	7	* LD A,SET 1,(IY+d)	7
#D0	RET NC	4   2	SET 2,B	2							* LD B,SET 2,(IX+d)	7	* LD B,SET 2,(IY+d)	7
#D1	POP DE	3	SET 2,C	2							* LD C,SET 2,(IX+d)	7	* LD C,SET 2,(IY+d)	7
#D2	JP NC,nn	3	SET 2,D	2							* LD D,SET 2,(IX+d)	7	* LD D,SET 2,(IY+d)	7
#D3	OUT (n),A	3	SET 2,E	2							* LD E,SET 2,(IX+d)	7	* LD E,SET 2,(IY+d)	7
#D4	CALL NC,nn	5   3	SET 2,H	2							* LD H,SET 2,(IX+d)	7	* LD H,SET 2,(IY+d)	7
#D5	PUSH DE	4	SET 2,L	2							* LD L,SET 2,(IX+d)	7	* LD L,SET 2,(IY+d)	7
#D6	SUB n	2	SET 2,(HL)	3							SET 2,(IX+d)	7	SET 2,(IY+d)	7
#D7	RST #10	4	SET 2,A	2							* LD A,SET 2,(IX+d)	7	* LD A,SET 2,(IY+d)	7
#D8	RET C	4   2	SET 3,B	2							* LD B,SET 3,(IX+d)	7	* LD B,SET 3,(IY+d)	7
#D9	EXX	1	SET 3,C	2							* LD C,SET 3,(IX+d)	7	* LD C,SET 3,(IY+d)	7
#DA	JP C,nn	3	SET 3,D	2							* LD D,SET 3,(IX+d)	7	* LD D,SET 3,(IY+d)	7
#DB	IN A,(n)	3	SET 3,E	2							* LD E,SET 3,(IX+d)	7	* LD E,SET 3,(IY+d)	7
#DC	CALL C,nn	5   3	SET 3,H	2							* LD H,SET 3,(IX+d)	7	* LD H,SET 3,(IY+d)	7
#DD			SET 3,L	2							* LD L,SET 3,(IX+d)	7	* LD L,SET 3,(IY+d)	7
#DE	SBC A,n	2	SET 3,(HL)	3							SET 3,(IX+d)	7	SET 3,(IY+d)	7
#DF	RST #18	4	SET 3,A	2							* LD A,SET 3,(IX+d)	7	* LD A,SET 3,(IY+d)	7
#E0	RET PO	4   2	SET 4,B	2							* LD B,SET 4,(IX+d)	7	* LD B,SET 4,(IY+d)	7
#E1	POP HL	3	SET 4,C	2	POP IX	4			POP IY	4	* LD C,SET 4,(IX+d)	7	* LD C,SET 4,(IY+d)	7
#E2	JP PO,nn	3	SET 4,D	2							* LD D,SET 4,(IX+d)	7	* LD D,SET 4,(IY+d)	7
#E3	EX (SP),HL	6	SET 4,E	2	EX (SP),IX	7			EX (SP),IY	7	* LD E,SET 4,(IX+d)	7	* LD E,SET 4,(IY+d)	7
#E4	CALL PO,nn	5   3	SET 4,H	2							* LD H,SET 4,(IX+d)	7	* LD H,SET 4,(IY+d)	7
#E5	PUSH HL	4	SET 4,L	2	PUSH IX	5			PUSH IY	5	* LD L,SET 4,(IX+d)	7	* LD L,SET 4,(IY+d)	7
#E6	AND n	2	SET 4,(HL)	3							SET 4,(IX+d)	7	SET 4,(IY+d)	7
#E7	RST #20	4	SET 4,A	2							* LD A,SET 4,(IX+d)	7	* LD A,SET 4,(IY+d)	7
#E8	RET PE	4   2	SET 5,B	2							* LD B,SET 5,(IX+d)	7	* LD B,SET 5,(IY+d)	7
#E9	JP (HL)	1	SET 5,C	2	JP (IX)	2			JP (IY)	2	* LD C,SET 5,(IX+d)	7	* LD C,SET 5,(IY+d)	7
#EA	JP PE,nn	3	SET 5,D	2							* LD D,SET 5,(IX+d)	7	* LD D,SET 5,(IY+d)	7
#EB	EX DE,HL	1	SET 5,E	2							* LD E,SET 5,(IX+d)	7	* LD E,SET 5,(IY+d)	7
#EC	CALL PE,nn	5   3	SET 5,H	2							* LD H,SET 5,(IX+d)	7	* LD H,SET 5,(IY+d)	7
#ED			SET 5,L	2							* LD L,SET 5,(IX+d)	7	* LD L,SET 5,(IY+d)	7
#EE	XOR n	2	SET 5,(HL)	3							SET 5,(IX+d)	7	SET 5,(IY+d)	7
#EF	RST #28	4	SET 5,A	2							* LD A,SET 5,(IX+d)	7	* LD A,SET 5,(IY+d)	7
#F0	RET P	4   2	SET 6,B	2							* LD B,SET 6,(IX+d)	7	* LD B,SET 6,(IY+d)	7
#F1	POP AF	3	SET 6,C	2							* LD C,SET 6,(IX+d)	7	* LD C,SET 6,(IY+d)	7
#F2	JP P,nn	3	SET 6,D	2							* LD D,SET 6,(IX+d)	7	* LD D,SET 6,(IY+d)	7
#F3	DI	1	SET 6,E	2							* LD E,SET 6,(IX+d)	7	* LD E,SET 6,(IY+d)	7
#F4	CALL P,nn	5   3	SET 6,H	2							* LD H,SET 6,(IX+d)	7	* LD H,SET 6,(IY+d)	7
#F5	PUSH AF	4	SET 6,L	2							* LD L,SET 6,(IX+d)	7	* LD L,SET 6,(IY+d)	7
#F6	OR n	2	SET 6,(HL)	3							SET 6,(IX+d)	7	SET 6,(IY+d)	7
#F7	RST #30	4	SET 6,A	2							* LD A,SET 6,(IX+d)	7	* LD A,SET 6,(IY+d)	7
#F8	RET M	4   2	SET 7,B	2							* LD B,SET 7,(IX+d)	7	* LD B,SET 7,(IY+d)	7
#F9	LD SP,HL	2	SET 7,C	2	LD SP,IX	3			LD SP,IY	3	* LD C,SET 7,(IX+d)	7	* LD C,SET 7,(IY+d)	7
#FA	JP M,nn	3	SET 7,D	2							* LD D,SET 7,(IX+d)	7	* LD D,SET 7,(IY+d)	7
#FB	EI	1	SET 7,E	2							* LD E,SET 7,(IX+d)	7	* LD E,SET 7,(IY+d)	7
#FC	CALL M,nn	5   3	SET 7,H	2							* LD H,SET 7,(IX+d)	7	* LD H,SET 7,(IY+d)	7
#FD			SET 7,L	2							* LD L,SET 7,(IX+d)	7	* LD L,SET 7,(IY+d)	7
#FE	CP n	2	SET 7,(HL)	3							SET 7,(IX+d)	7	SET 7,(IY+d)	7
#FF	RST #38	4	SET 7,A	2							* LD A,SET 7,(IX+d)	7	* LD A,SET 7,(IY+d)	7

Les instructions précédées du caractère \* ne sont pas documentées.

## Jeu d'instructions du Z80A

### Décalages et rotations :



Les instructions précédées du caractère \* ne sont pas documentées.